ATTY DOCKET NO. APPLICATION NO. 5035-203US//P29.654 USA 10/518,558 INFORMATION DISCLOSURE CITATION APPLICANT(S) se several sheets if necessary) Richard Michael Taylor FILING DATE GROUP ART UNIT Dec. 22, 2004 **U.S. PATENT DOCUMENTS** EXAMINER FILING DATE DOCUMENT NUMBER DATE NAME CLASS SUBCLASS INITIAL IF APPROPRIATE 5,819,067 06/10/1998 Lynch, Thomas W. 703 22 KK 6,298,434 02/10/2001 Lindwer, Menno M. KK 712 209 KK 6,308,255 23/10/2001 Gorishek, Frank J. et al 712 209 6,031,992 29/02/2000 -Wing, Malcom-John et al KK Robert F. 717 138 Cmel et al U.S. PATENT APPLICATION PUBLICATIONS *EXAMINER FILING DATE DOCUMENT NUMBER DATE NAME CLASS SUBCLASS INITIAL IF APPROPRIATE 2002 013892 31/01/2002 Gorishek, et al. KK 712 227 **FOREIGN PATENT DOCUMENTS** TRANSLATION DOCUMENT NUMBER DATE COUNTRY CLASS SUBCLASS YES NO EP 0 945 788 A2 29/09/1999 **EPO** KK 2 350 216 22/11/2000 UK KK 2 366 426 06/03/2002 UK KK WO 03/044977 30/05/2003 PCT KK WO 09406077 17/3/1994 KK **PCT** abs OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) "PipeRench Implementation of the Instruction Path Coprocessor"; Chou; Pillai; Schmit; Shen Department of Electrical and Computer Engineering - Carnegie Mellon University Pittsburgh, PA 15213; pages 1-12 KK **EXAMINER DATE CONSIDERED** /Kenneth Kim/ 09/08/2006 *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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